

2823 Jhs

October 31, 2004

To: Commissioner for Patents P.O Box 1450 Alexandria, VA 22313-1450

Fr: Stephen B. Ackerman, Reg. No. 37,761 28 Davis Ave. Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/628,913

07/29/03

Y. T. Chan et al

"METHOD OF FORMING A PARTIALLY DEPLETED SILICON ON INSULATOR (PDSOI) TRANSISTOR WITH A PAD LOCK BODY EXTENSION"

GRP. Art. Unit 2823

T. Q. Dang

## RESPONSE PATENT OFFICE ACTION

Dear Sir;

In response to an Office Action dated October 05, 2004, please amend the identified application for patent as follows:

The commissioner is hereby authorized to charge payment of any additional fees involved with added Claims and the like to Deposit Account No. 19-0033

## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on January 5, 20%.

Stephen B. Ackerman, Reg. No. 37,761

Signature

Date

## CS02-068

Amendments to Claims begin on page 3

Remarks begin on page 9